



A Two-Day National Conference

On

**Recent Advances in Power
Electronics, Power and Control
Systems Engineering**

(RAPEPCSE-2016)

6th-7th October 2016

Under TEQIP-II



Organized by

Department of Electrical Engineering
AUCollege of Engineering (A)
Andhra University
Visakhapatnam-530003, A.P. India

Organizing committee

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Prof.G.Nageswara Rao,
Hon'ble Vice Chancellor
Andhra University, Visakhapatnam

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Dept. of Elect. Engg., AUCE(A)

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Dept. of Elect. Engg., AUCE(A)

Co-ordinators

Dr. K. Padma
Dr.R.Srinu Naik
(Dr).B.Amarendra Reddy
Dr.R.Vijaya Santhi
Assistant Professors
Dept. of Elect. Engg., AUCE(A)

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About AUCE(A)

Andhra University College of Engineering (A) grew from strength to strength. Today the Campus offers 17 UG and 35 PG programmes besides research on par with technical institutions worldwide. The strength of the college is its faculty who motivate the students in leading the country in its ongoing technological revolution and entrepreneurial skills. All the departments are running Ph.D. programmes in different fields of research.

About EE Department

The department of Electrical Engineering was started in 1955 as one of the three constituents of the Department of Engineering in Andhra University and currently offers a UG program in Electrical and Electronics Engineering and PG programs in Power Systems & Automation, Control Systems Engineering and Power Electronics Drives and Control. The department initiated developmental activities in the emerging areas of CAD and Robotics with MHRD grants. The department has well established Machines, Networks, Measurements, Power Electronics, Micro-Processors, Control Systems, Power System Simulation, Renewable Energy Systems & Digital Electronics Laboratories. The department had undertaken consultancy projects in collaboration with NSTL, AICTE etc..

Objective of the Conference

The objective of the conference is to bring Engineers, Scientists, Researchers and Students to single platform to share their knowledge and ideas in the recent trends in the field of power electronics, power systems and control systems. The conference also providing facilities for discussing between various participants to improve their knowledge, research ability and group works/collaboration works.

Areas to be covered, but not limited to –

Track 1–Power Systems

- Power System Stability, Dynamics and Control
- Power System Economics and Policies
- Distributed Generation and Micro Grid
- Smart Grid and Smart Technology
- Power System Protection and Relaying
- Energy Mgt and Sustainable Energy
- Power System Reliability Assessment

Track II–Power Electronics

- Power Electronics and Electrical Drives
- Power Electronics Applications in Power System
- Application of Soft Computing Techniques in Electrical System, Renewable Energy and Hybrid Energy Systems
- Power Quality Issues
- Multilevel Converters for Drives and Renewable Energy
- Multiphase and Multi-winding Electrical Motors and Generators
- Fault Tolerant Control of Electrical Machines and Drives
- Energy Storage Integration
- Control of AC/DC Micro-Grid

Track III–Control Systems

- Advanced Control Strategies and Applications
- Fault Identification
- Intelligent Control
- Decentralized Control
- Robust Control
- Noise and Vibration Control
- Process Control
- Uncertain Systems
- Robotics
- Modeling and System Identification

PRE-CONFERENCE TUTORIALS

Pre-conference tutorials will be held on 6th October, 2016 on the art topic "Voltage stability aspects in power transmission system and its mitigation using FACTS devices".

Keynote Speaker: Prof. Abhijit Chakraborty, Professor, IIT, Shibpur., Former VC, Jadavpur University, Kolkata, West Bengal 700032

Participants those who have registered for conference can attend for Pre-conference tutorials.

Eligibility

Faculty members of Engineering / Polytechnic Colleges approved by AICTE / Researchers / Practicing Engineers / Ph.D scholars and PG students are eligible.

Registration

- Industrial: Rs 2000/-
- Faculty members: Rs.1500/-
- Student members: Rs.1000/-

Participants can register by sending the registration form along with Demand Draft in favour of “Dr.K.Padma, Coordinator, RAPEPCSE-2016, Andhra Bank, AUCE(A) Branch, Visakhapatnam” and it should reach on or before 2nd, October 2016.

IMPORTANT DATES

Full Length Paper Submission: 20th September 2016
Acceptance Notification: 25th September 2016
Early Registration: 27th September 2016
Camera Ready Submission: 2nd October 2016

Note:

- Full length papers should be submitted within 6-8 pages electronically in IEEE double column format.
- Accommodation will be provided on payment basis subject to availability

Registration form

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1.Participant Name:

2.Qualification & Experience:

3.Department:

4.Organization:

5.Address for Correspondence:

6.Phone No.:

7.E-mail :

8.Registration fee:

9.Bank details
DD .No, date :

10 Accommodation: yes/no

Declaration

The information furnished above is true to the best of my knowledge. I agree to abide by the rules and regulations governing the conference.

Place:

Date: Signature of the Applicant

ADDRESS FOR CORRESPONDENCE

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