

**M.Tech (EI), Two Year (Four Semesters)**  
Scheme to be valid with effect from the admitted batch of 2019 – 2020

**SEMESTER-I**

Code	Name of the Subject	Periods/Week		Max. Marks		Total	Credits
		Theory	Lab	Ext.	Int.		
MTEI-1.1	Digital Signal Processing	3	-	70	30	100	3
MTEI-1.2	Electronic Instrumentation Techniques	3	-	70	30	100	3
MTEI-1.3	Elective I DSP Processors and Architecture/ Wireless communication and Networks/ Microprocessor systems	3	-	70	30	100	3
MTEI-1.4	Elective II EMI/EMC/ Bio Medical Instrumentation/ Artificial Intelligence and Neural Networks	3	-	70	30	100	3
MTEI-1.5	Research Methodology & IPR	3	-	70	30	100	2
MTEI-1.6	Audit Course	3	-	70	30	100	0
MTEI-1.7	Instrumentation LAB-I	-	3	-	100	100	2
MTEI-1.8	Signal Processing LAB	-	3	-	100	100	2
	<b>Total</b>	<b>18</b>	<b>06</b>	<b>420</b>	<b>380</b>	<b>800</b>	<b>18</b>

**SEMESTER-II**

Code	Name of the Subject	Periods/Week		Max. Marks		Total	Credits
		Theory	Lab	Ext.	Int.		
MTEI-2.1	Transducers and signal conditioners	3	-	70	30	100	3
MTEI-2.2	Data Acquisition Systems	3	-	70	30	100	3
MTEI-2.3	Elective – III Processor Control Instrumentation /Remote Sensing and Image Sensors /GPS and Applications		-	70	30	100	3
MTEI-2.4	Elective – IV Nano Technology and Applications /Microcontrollers and Embedded Systems /Digital Image Processing	3	-	70	30	100	3
MTEI-2.5	Audit Course	3	-	70	30	100	0
MTEI-2.6	Instrumentation LAB-II	-	3	-	100	100	2
MTEI-2.7	Microprocessor LAB	-	3	-	100	100	2
MTEI-2.8	Mini Project With Seminar	-	3	-	100	100	2
	<b>Total</b>	<b>15</b>	<b>9</b>	<b>350</b>	<b>450</b>	<b>800</b>	<b>18</b>

### SEMESTER-III

Code	Name of the Subject	Periods/Week		Max. Marks		Total	Credits
		Theory	Lab	Ext.	Int.		
MTEI-3.1	Elective V Linear and Digital Systems Design / Optical Fibers and Applications/ /Application specific Integrated circuits(ASIC)	3	-	70	30	100	3
MTEI-3.2	Open Elective VLSI/ Business Analytics Industrial Safety Operational Research Cost Management of Engineering Projects	3	-	70	30	100	3
MTEI-3.3	Dissertation- I / Industrial Project	-	-	-	100	100	10
	Total	6		140	160	300	16

### SEMESTER-IV

Code	Name of the Subject	Periods/Week		Max. Marks		Total	Credits
		Theory	Lab	Ext.	Int.		
MTEI-4.1	Dissertation- II	-	-	70	30	100	16
	Total			70	30	100	16

#### **Note:**

1. At the end of 3<sup>rd</sup> semester project review is conducted by HOD with the committee consisting of the HOD, Chair Person of BOS and the Guide. In the Affiliated Colleges, Project (Preliminary) will be evaluated by concerned HOD and the thesis Guide of their respective colleges.
2. At the end of the 4<sup>th</sup> semester there will be a final viva voce for the project work conducted by the HOD with the committee consisting of HOD, Chair Person of BOS, the Guide and an External examiner nominated by the university.

#### **Audit Course 1 & 2**

1. English for Research Paper Writing
2. Disaster Management
3. Sanskrit for Technical Knowledge
4. Value Education
5. Constitution of India
6. Pedagogy Studies
7. Stress Management by Yoga
8. Personality Development through Life Enlightenment Skills.

# DIGITAL SIGNAL PROCESSING

**Subject Code:** MTEI – 1.1  
**Semester-I**

**Credits: 3**  
**Exam Marks: 70**  
**Sessional: 30**

Chapter – I : Advanced digital filter design techniques : Multiple band optimal FIR filters – design of filters with simultaneous constraints in time and frequency response, optimization methods for designing IIR filters, comparison of optimum FIR filters and delay equalized elliptic filters.

Chapter – II : Multirate DSP : The basic sample rate alteration – time – domain characterization, frequency – domain characterization : Cascade equivalences, filters in sampling rate alteration systems, digital filter banks and their analysis and applications, multi level filter banks, estimations of spectra from finite – duration observation of signals.

Chapter – III : linear prediction and optimum linear filters : forward and backward linear prediction, AR Lattice and ARMA lattice – ladder filters, Wiener filters for filtering on prediction.

Chapter – IV : DSP Algorithms : The Goertzel algorithm, the chirp – z transform algorithm the Levinson – Durbin algorithms, the Schur algorithm, and other algorithms, computations of the DFT, concept of tunable digital filters.

Chapter – V : Signal Processing Hardware : Multipliers, dividers, different forms of FIR Hardware, multiplexing, DTTR, TDM to FDM translator, realization of frequency synthesizer, FET hardware realization, different FFT architectures, special FFT processors, convolvers, Lincoln laboratory FDP and the compatible computer configurations.

Chapter – VI : Applications of DSP :

- a) Speech : Model of speech production, speech analysis – synthesis system vocoder analyzers and synthesizers, linear prediction of speech.
- b) DTMF System

## **Suggested Books :**

1. Theory and applications of digital signal processing by Lawrence R. Rabiner and Bernard Gold, PHI
2. Digital Signal Processing. Principles, algorithms, and applications by John G. Proakis and Dimitris G. Manolakis, PHI, 1997.
3. Digital Signal Processing, A Computer – Based approach, by Sanjit K. Mitra, Tata Mc Graw-Hill, 1998

# ELECTRONIC INSTRUMENTATION TECHNIQUES

**Credits: 3**

**Subject Code: MTEI – 1.2**

**Exam Marks: 70**

**Semester-I**

**Sessional: 30**

Principles and Design of Electronic Instruments : Digital voltmeters, Electronic counters, Frequency synthesizers, Wave analysers, Spectrum analysers, Sweep waveform generators and pulse generators, Lock-in amplifiers, Q-meters, High frequency impedance bridges, Ground loops, Electromagnetic and static pick-up, Interference, Shielding and grounding, Floating voltage measurements; Common signals and their effects.

Oscilloscopes: Sweep generators, Sweep modes, Storage oscilloscopes types, Erasing methods, Sampling oscilloscopes synchronous and random sampling, Time domain reflectometry, Logic state analysers and their applications.

Display System : Liquid crystal, Solid state CRT, Displays,

Recorders : Servo magnetic, U-V recorders, X-Y plotters. Computer based automated test instruments.

## **Text Book :**

1. B.M. Oliver and J.M. Cage, ELECTRONIC MEASUREMENTS AND INSTRUMENTATION Kogakusha-McGraw Hill.

## **Reference :**

1. Manufacturer's Literature.

## **Elective-I**

# **MICROPROCESSOR SYSTEMS**

**Subject Code:** MTEI – 1.3  
**Semester-I**

**Credits: 3**  
**Exam Marks: 70**  
**Sessional: 30**

Introduction : Historical background, Microprocessor based personal computer systems, RISC processor, Micro controllers, comparison of 8048, 8049, 8051 and 8052, Architecture of 8051.

Introduction and comparison of 8086, 8088, 80186 / 80188, 80286, 80386, 80486, Pentium and Pentium – Pro Processors, Addressing modes, Memory and Architecture. 8086 / 8088 Hardware specifications – Memory interface – I/O Interface – Interrupts – DMA – The Arithmetic Coprocessor Bus Interface - 8086 / 8088 Addressing Modes – Instructions – Programming.

### **References :**

1. The Intel Microprocessors 8086 / 8088, 80186, 80188, 80286, 80386, 80486, Pentium and Pentium – Pro Processor Architecture, Programming and Interface by Barry B. Berry, 4<sup>th</sup> Edition, PHI.
2. Microprocessors Principles and Applications by Gilmore, 2<sup>nd</sup> Edition, TMH.
3. Microprocessors and Interfacing Programming and Applications by Douglas V. Hall, Mc Graw Hill.
4. Microprocessors / Microcomputers Architecture, Software and Systems by A.J. Khambata, John Wiley & Sons.
5. Advanced Microprocessors by Daniel Tabak, Mc Graw Hill, 1995.

# **Elective-I**

## **DSP PROCESSORS & ARCHITECTURES**

**Credits: 3**

**Subject Code: MTEI – 1.3**

**Exam Marks: 70**

**Semester-I**

**Sessional: 30**

### **UNIT I**

#### **INTRODUCTION TO DIGITAL SIGNAL PROCESING**

Introduction, A Digital signal-processing system, The sampling process, Discrete time sequences Discrete Fourier Transform (DFT) and Fast Fourier Transform (FFT), Linear time-invariant systems, Digital filters, Decimation and interpolation, Analysis and Design tool for DSP Systems MATLAB, DSP using MATLAB.

### **UNIT II**

#### **COMPUTATIONAL ACCURACY IN DSP IMPLEMENTATIONS**

Number formats for signals and coefficients in DSP systems, Dynamic Range and Precision, Sources of error in DSP implementations, A/D Conversion errors, DSP Computational errors, D/A Conversion Errors, Compensating filter.

### **UNIT III**

#### **ARCHITECTURES FOR PROGRAMMABLE DSP DEVICES AND EXECUTION**

Basic Architectural features, DSP Computational Building Blocks, Bus Architecture and Memory, Data Addressing Capabilities, Address Generation Unit, Programmability and Program Execution, Speed Issues, Features for External interfacing, Hardware looping, Interrupts, Stacks, Relative Branch support, Pipelining and Performance, Pipeline Depth, Interlocking, Branching effects, Interrupt effects, Pipeline Programming models

### **UNIT IV**

#### **PROGRAMMABLE DIGITAL SIGNAL PROCESSORS**

Commercial Digital signal-processing Devices, Data Addressing modes of TMS320C54XX DSPs, Data Addressing modes of TMS320C54XX Processors, Memory space of TMS320C54XX Processors, Program Control, TMS320C54XX instructions and Programming, On-Chip Peripherals, Interrupts of TMS320C54XX processors, Pipeline Operation of TMS320C54XX Processors.

### **UNIT V**

#### **IMPLEMENTATIONS OF BASIC DSP ALGORITHMS AND FFT ALGORITHMS**

The Q-notation, FIR Filters, IIR Filters, Interpolation Filters, Decimation Filters, PID Controller, Adaptive Filters, 2-D Signal Processing, An FFT Algorithm for DFT Computation, A Butterfly Computation, Overflow and scaling, Bit-Reversed index generation, An 8-Point FFT implementation on the TMS320C54XX, Computation of the signal spectrum

### **UNIT VI**

#### **INTERFACING MEMORY AND I/O PERIPHERALS TO PROGRAMMABLE DSP DEVICES**

Memory space organization, External bus interfacing signals, Memory interface, Parallel I/O interface, Programmed I/O, Interrupts and I/O, Direct memory access (DMA). A Multichannel

buffered serial port (McBSP), McBSP Programming, a CODEC interface circuit, CODEC programming, A CODEC-DSP interface example.

**TEXT BOOKS:**

1. Digital Signal Processing – Avtar Singh and S. Srinivasan, Thomson Publications, 2004.
2. DSP Processor Fundamentals, Architectures & Features – Lapsley et al. S. Chand & Co, 2000.

**REFERENCES:**

1. Digital Signal Processors, Architecture, Programming and Applications – B. VenkataRamani and M.Bhaskar, TMH, 2004.
2. Digital Signal Processing – Jonatham Stein, John Wiley, 2005.

**Elective-I**  
**WIRELESS COMMUNICATION AND NETWORKS**

**Credits: 3**

**Subject Code: MTEI – 1.3**

**Exam Marks: 70**

**Semester-I**

**Sessional: 30**

**UNIT -I**

**WIRELESS COMMUNICATION AND SYSTEM FUNDAMENTALS:** Introduction to Wireless Communication Systems, Examples of Wireless Communications, Comparisons of Common Wireless Communication Systems, Trends in Cellular Radio and Personal Communications, Cellular Concepts, Frequency Reuse, Handoff Strategies, Interference and System Capacity, Trucking and Grade Of Service, Improving Coverage & Capacity In Cellular Systems.

**UNIT-II**

**MULTIPLE ACCESS TECHNIQUES FOR WIRELESS COMMUNICATION:** FDMA, TDMA, SSMA (FHMA/CDMA/Hybrid Techniques) SDMA Technique (As Applicable to Wireless Communications), Packet Radio Access Protocols, CSMA Protocols, Reservation Protocols, Capture Effect in Packet Radio, Capacity of Cellular Systems.

**UNIT-III**

**WIRELESS NETWORKING:** Introduction, Differences Between Wireless and Fixed Telephone Networks, Traffic Routing in Wireless Networks, Circuit Switching, Packet Switching, The X.25 protocol.

**UNIT-IV**

Wireless Data Services, Cellular Digital Packet Data (CDPD), Advanced Radio Data Information Systems (ARDIS), RAM Mobile Data (RMD), Common Channel Signaling (CCS), Broad Band ISDN and ATM, Signaling System .No.7 (SS7), Network Services Part (NSP), SS7 User Part, Signaling Traffic in SS7, SS7 Services, Performance of SS7.

**UNIT-V**

**MOBILE IP AND WIRELESS APPLICATION PROTOCOL:** Mobile IP Operation of Mobile IP, Co-located Address, Registration, Tunneling, WAP Architecture, Overview, WML Scripts, WAP Service, WAP Session protocol, Wireless Transaction, Wireless Datagram, Infrared LAN's, Spread Spectrum LAN's, Narrowband Microwave LAN's, IEEE 802 Protocol Architecture, IEEE 802 Architecture and Services, 802. 11 Medium Access Controls, 802.11 Physical Layers.

**UNIT-VI**

**BLUE TOOTH AND MOBILE DATA NETWORKS:** Overview, Radio Specification, Baseband Specification, Links Manager Specification, Logical Link Control and Adaptation Protocol, Introduction to WLL Technology, Introduction, and Data Oriented CDPD Network, GPRS and Higher Data Rates, Short Messaging Service in GSM, Mobile Application Protocol.

**TEXT BOOKS:**

1. Wireless communication and Networking -William Stallings, PHI, 2003
2. Wireless Communications, Principles, Practice - Theodore, S. Rappaport, PHI, 2<sup>nd</sup> Edition, 2002.

**REFERENCES:**

1. Wireless Digital Communications-Karnilofeher, PHI, 1999.
2. Principles of Wireless Networks - Kavehpahlaven and P.Krishna Murthy, Pearson Education, 2002



## **Elective-II** **EMI/EMC**

**Credits: 3**

**Subject Code: MTEI – 1.4**

**Exam Marks: 70**

**Semester-I**

**Sessional: 30**

- I. Introduction, Natural and Nuclear sources of EMI / EMC :  
Electromagnetic environment, History, Concepts, Practical experiences and concerns, frequency spectrum conservations. An overview of EMI / EMC, Natural and Nuclear sources of EMI.
- II. EMI from apparatus, circuits and open area test sites :  
Electromagnetic emissions, noise from relays and switches, non-linearities in circuits, passive intermodulation, cross talk in transmission lines, transients in power supply lines, electromagnetic interference (EMI). Open area test sites and measurements.
- III. Radiated and conducted interference measurements and ESD :  
Anechoic chamber, TEM cell, GH TEM Cell, characterization of conduction currents / voltages, conducted EM noise on power lines, conducted EMI from equipment, Immunity to conducted EMI detectors and measurements. ESD, Electrical fast transients / bursts, electrical surges.
- IV. Grounding, shielding, bonding and EMI filters:  
Principles and types of grounding, shielding and bonding, characterization of filters, power lines filter design.
- V. Cables, connectors, components and EMC standards :  
EMI suppression cables, EMC connectors, EMC gaskets, Isolation transformers, optoisolators, National / International EMC standards.

### **Text Books:**

1. Engineering Electromagnetic Compatibility by Dr. V.P. Kodali, IEEE Publication, Printed in India by S. Chand & Co. Ltd., New Delhi, 2000.
2. Electromagnetic Interference and Compatibility IMPACT series, IIT – Delhi, Modules 1– 9.

### **Reference:**

1. Introduction to Electromagnetic Compatibility, Ny, John Wiley, 1992, by C.R. Pal.

**Elective-II**  
**BIO MEDICAL INSTRUMENTATION**

**Credits: 3**

**Subject Code:** MTEI – 1.4

**Exam Marks:** 70

**Semester-I**

**Sessional:** 30

**Chapter – I:** Sources of Bioelectric potentials and Electrodes

Resisting and Action Potentials, Propagation of Action Potentials, The Bioelectric Potentials  
Electrode theory, Bio Potential Electrodes, Biochemical Transducers

**Chapter – II:** The Cardiovascular System and Cardiovascular Measurements,

The Heart and Cardiovascular System, The Heart, Blood Pressure, Characteristics of Blood Flow,  
Heart Sounds

Electrocardiography, Measurement of Blood Pressure, Measurement of Blood Flow and Cardiac  
output, Plethysmography, Measurement of Heart Sounds,

**Chapter – III:** Patient Care & Monitory and Measurements in Respiratory System

The elements of Intensive Care Monitory, Diagnosis, Calibration and repairability of Patient Monitoring  
equipment, other instrumentation for monitoring patients, pace makers, defibrillators

The Physiology of respiratory system, tests and instrumentation for mechanics of breathing,  
respiratory theory equipment

**Chapter – IV:** Bio telemetry and Instrumentation for the clinical laboratory Introduction to  
biotelemetry, physiological parameters adaptable to biotelemetry, the components of biotelemetry  
system, implantable units, applications of telemetry in patient care

The blood, tests on blood cells, chemical test, automation of chemical tests

**Chapter – V:** X – ray and radioisotope instrumentation and electrical safety of medical equipment.

Generation of Ionizing radiation, instrumentation for diagnostic X – rays, special techniques,  
instrumentation for the medical use of radioisotopes, radiation therapy.

Physiological effects of electrical current, shock Hazards from electrical equipment, Methods of  
accident prevention

**TEXT BOOK:**

1. Biomedical Instrumentation and Measurements – C. Cromwell, F.J. Weibell, E.A. Pfeiffer –  
Pearson education.

**Elective-II**  
**ARTIFICIAL INTELLIGENCE AND NEURAL NETWORKS**

**Credits: 3**

**Subject Code: MTEI – 1.4**

**Exam Marks: 70**

**Semester-I**

**Sessional: 30**

**Artificial Intelligence as Representation and Search**

Introduction to AI, Roots and Scope of AI, Definition, Turing Test, Application Areas of AI, Predicate Calculus , Structures and Strategies for State Space Search , Heuristic Search , Control and Implementation of State Space Search

Representation and Inference

Knowledge Representation , Strong Methods for Problem Solving , Reasoning in Uncertain , Situations, Machine Learning : Symbol-Based: Framework for Symbol – Based Learning, Version Space Search, ID3 Algorithm, Un-supervised learning, Reinforcement Learning , Connectionist: Perceptron Learning, Backpropagation Learning, Competitive Learning, Hebbian Coincidence Learning, Attractor Networks

**Neural Networks and Fuzzy Systems**

Neural and Fuzzy machine intelligence, fuzziness as multivalence, the dynamical-systems approach to machine intelligence, intelligent behaviour as adaptive model-free estimation.

**Neural Dynamics**

I. Activations and signals : Neurous as functions, signal monotonicity, biological activations and signals, neuron fields, neuronal dynamical systems, common signal functions, pulse-coded signal functions, Neuronal dynamics II : Activation Models : neuronal dynamical systems, additive neuronal dynamics, additive neuronal feedback, additive bivalent models, BAM Connection matrices, additive dynamic and the noise-saturation dilemma, general neuronal activations : Cohen-grossberg and multiplicative models

**Synaptic Dynamics**

I. Unsupervised Learning : Learning as encoding, change, and quantization, four unsupervised learning laws, probability spaces and random processes, stochastic unsupervised learning and stochastic equilibrium, signal hebbian learning, competitive learning, differential hebbian learning, differential competitive learning. Synaptic Dynamics II : Supervised learning : Supervised function estimation, supervised learning as operant conditioning, supervised learning as stochastic pattern learning with known class memberships, supervised learning as stochastic approximation, the back propagation algorithm.

**Text Books:**

1. “Artificial Intelligence – Structures and Strategies for Complex Problem Solving”, George F. Luger, 4<sup>th</sup> Edition, Pearson Education , 2003.
2. Neural Networks & Fuzzy Systems, Bark Kosko, PHI Published in 1994.

**Reference Books:**

1. “Artificial Intelligence”, Knight, Tata McGraw Hill
2. “Artificial Intelligence ‘a Modern Approach” Russell &Norvig, second edition , Pearson Education , 2003.

3. Fundamentals of Artificial Neural Networks, Mohamad H Hassoum, PHI
4. Neural Network Design, Hagan, Demuth and Beale, Vikas Publishing House.

**RESEARCHMETHODOLOGY AND IPR**

**Subject Code: MTEI – 1.5**

**Semester-I**

**Credits: 2**

**Exam Marks: 70**

**Sessional: 30**

**AUDIT COURSE**

**Subject Code:** MTEI – 1.6

**Semester-I**

**Credits: 0**

**Exam Marks: 70**

**Sessional: 30**

## **INSTRUMENTATION LAB-I**

**Subject Code:** MTEI – 1.7

**Credits:** 2

**Exam Marks:** 70

**Sessional:** 30

**SIGNAL PROCESSING LAB**

**Subject Code: MTEI – 1.8**  
**Semester-I**

**Credits: 2**  
**Exam Marks: 100**



## **TRANSDUCERS AND SIGNAL CONDITIONERS**

**Credits: 3**

**Subject Code: MTEI – 2.1**

**Exam Marks: 70**

**Semester-II**

**Sessional: 30**

Unframed theory of bilateral electromechanical transducers, sensitivity and linearity analysis, static and dynamic responses, transfer function analysis of various transducers and their associated circuits, electrodynamic variable, variable inductance, variable capacitance, piezoelectric and force – balance transducers.

Design and construction of the above types of transducers :

Uses of the above transducers in measurement of displacement, velocity and acceleration. Thermo couples, Quartz thermometers, transducers for pressure and flow measurements. Ionization gages, load cell and force – balanced transducers. Doppler shift flow meter, thermal transport flow meter. Magnetic flow meter.

### **Text Books:**

1. H.K.P. Neubert Instrument Transducers Oxford University Press : (Second edition)
2. E.O. Doebelin 'Measurement Systems' Mc Graw Hill.

## **DATA ACQUISITION SYSTEM**

**Subject Code:** MTEI – 2.2

**Semester-II**

**Credits: 3**

**Exam Marks: 70**

**Sessional: 30**

Data Acquisition system: Introduction, Principles and design.

Digital to Analog converters (DACs) : Parallel R-2R, Weighted resistor, inverted ladder and serial (ADCs).

Analog to Digital Converters (ADCs) : Paralleled feedback, Successive approximation, Ramp comparison, Dual slope integration, Voltage to frequency, Voltage to time, Logarithmic types of ADCs, Accuracy analysis, Dynamic and static error analysis of the above, Typical study of monolithic DACs and ADCs.

### **Text Books**

1. H. Schmid 'ELECTRONIC ANALOG-DIGITAL CONVERSION' McGraw Hill
2. D.G. Hoeschele 'A to D and D to A conversion techniques' Wiley
3. B.S. Sopnde Data Converters – Tata McGraw Hill
4. Analog Devices – Handbook.

### **References**

1. E.R. Hanateck, User's Handbook of D/A and A/D converters – Wiley.
2. Datel / Intersil – Data acquisition systems.

**Elective-III**  
**GPS and APPLICATIONS**

**Credits: 3**

**Subject Code: MTEI – 2.3**

**Exam Marks: 70**

**Semester-II**

**Sessional: 30**

Unit I

Overview of GPS :

Basic concept, system architecture, space segment, user segment, GPS aided Geo-augmented navigation (GAGAN) architecture.

Unit II GPS Signals

Signal structure, anti spoofing (AS), selective availability, Difference between GPS and GALILEO satellite construction.

Unit III

GPS coordinate frames, Time references : Geodetic and Geo centric coordinate systems, ECEF coordinate world geodetic 1984 (WGS 84), GPS time.

Unit IV

GPS orbits and satellite position determination : GPS orbital parameters, description of receiver independent exchange format (RINEX) – Observation data and navigation message data parameters, GPS position determination.

Unit V

GPS Errors :

GPS error sources – clock error, ionospheric error, tropospheric error, multipath, ionospheric error estimation using dual frequency GPS receiver.

**Textbook:**

1. G S RAO, **Global Navigation Satellite Systems**, McGraw-Hill Publications, New Delhi, 2010

**Reference Books:**

1. B. Hoffman – Wellenhof, H. Liehtenegger and J. Collins, ‘GPS – Theory and Practice’, Springer – Wien, New York (2001).
2. James Ba – Yen Tsui, ‘Fundamentals of GPS receivers – A software approach’, John Wiley & Sons (2001).

## **Elective-III**

### **REMOTE SENSING AND IMAGE SENSORS**

**Credits: 3**

**Subject Code: MTEI – 2.3**

**Exam Marks: 70**

**Semester-I**

**Sessional: 30**

#### **Unit-I Basics of Remote Sensing**

- Principles of Remote sensing, History of Remote sensing, Remote sensing in India,
- Electromagnetic Radiation and Electromagnetic Spectrum, EMR quantities: Nomenclature and Units
- Thermal Emission of Radiation, Radiation Principles ( Plank's Law, Stephen Boltzman law), Interaction of EMR with the Earth Surface ( Wien's displacement law, Kirchoffs Law)
- Spectral signature, Reflectance characteristics of Earths cover types, Remote sensing systems.

#### **Unit – II Platforms and sensors**

- Platforms, Remote sensing sensors, resolutions Across track and along the track scanning, Optical sensors,
- Thermal scanners
  - Microwave sensing radar
  - satellite missions
  - Landsat series, SPOT series, IRS satellite series, IKONOS,

#### **Unit-III Microwave Remote Sensing**

- Airborne and Space borne radar systems basic instrumentation.
- System parameters - Wave length, Polarization, Resolutions, Radar geometry.
- Target parameters - Back scattering, Point target, Volume scattering, Penetration, Reflection, Bragg resonance, Cross swath variation. Speckle radiometric calibration.
- Radar - Geometry - Introduction, Mosaicing Stereoscope.
- Application : Geology, Forestry, Land use, Soils etc. Future trends and Research

#### **Unit-IV Thermal Imaging system**

- Thermal Imaging System: Introduction - IR region of the Electromagnetic spectrum, Atmospheric transmission, Kinetic and radiant temperature, Thermal properties of materials, Emissivity, Radiant temperature. Thermal conductivity. Thermal capacity, thermal inertia, Apparent thermal inertia, Thermal diffusivity.
- IR - radiometers, Airborne and Satellite TTR scanner system
- Characteristics of IR images
  - i) Scanner distortion, ii) image irregularities, iii) Film density and recorded
  - iv) Temperature ranges
- Effects of weather on images
  - i) Clouds, ii) Surface winds, iii) Penetration of smoke plumes
- Interpretation of thermal imagery
- Advantages of Thermal imagery

### **Unit-V**

- Meteorological satellites
- Meteorological satellite characteristics and their orbits, TIROS, NIMBUS, NOAA, TIROS N, SEASAT, GOES, METEOSAT, INSAT
- Measurement of Earth and Atmospheric energy and Radiation budget parameters from satellites

### **Text books**

1. Imaging Radar for Resource Survey: Remote Sensing Applications, 3, W Travelt, Chapman & Hall
2. Remote Sensing: The quantitative approach, P.H. Swain and S.M. Davis, McGraw Hill
3. Floyd, F. Sabins, Jr: Remote Sensing Principles and Interpretation, Freeman and Co. San Francisco, 1978
4. Applied Remote Sensing C.P.L.O., Longman Scientific and Technical Publishers.
5. Introduction to Environmental Remote Sensing, E.C. Barrett & L.F Curtis, Chapman and Hall, London
6. Fundamentals of remote sensing, George Joseph, Universities Press

## **Elective-III** **PROCESS CONTROL INSTRUMENTATION**

**Subject Code:** MTEI – 2.3

**Semester-II**

**Credits: 3**

**Exam Marks: 70**

**Sessional: 30**

**Elective-IV**  
**NANO TECHNOLOGY AND APPLICATIONS**

**Subject Code:** MTEI – 2.4

**Semester-II**

**Credits: 3**

**Exam Marks: 70**

**Sessional: 30**

Unit 1 : Introduction to Nanotechnology

Essence of Nanotechnology, Nano in daily life, Brief account of nano applications, Properties of nano materials, Metal nano clusters, Semiconductor nano particles.

Unit 2 : Nano Materials

Nano composites, Nanofying electronics, Sensing the environment, Mechanising the micro world, Energy and cleaner environment with nano technology.

Unit 3 : Carbon Nano Structures

Introduction, Carbon molecules, Carbon clusters, Carbon nanotubes, Applications of carbon nanotubes.

Unit 4 : Diagnosing Personal Health and Medical Applications

Lab on a chip, Super X-ray vision, Mapping the genes, Understanding how pharmaceutical company develops drugs, Delivering a new drug the Nanotech way, Cooking cancer with nano cells, Biomimetics.

Unit 5 : Biological Materials

Introduction, Biological building blocks, Nucleic acids, Biological nanostructures.

**Textbooks**

1. Nanotechnology by Richard Booker, Earl Boysen, Wiley Publishing Inc., 2006.
2. Introduction to Nanotechnology by Charles P. Poole Jr., Frank J. Owens, John Wiley & Sons Publications, 2003.

## Elective-IV

### MICROCONTROLLERS AND EMBEDDED SYSTEMS

**Credits: 3**

**Subject Code: MTEI – 2.4**

**Exam Marks: 70**

**Semester-II**

**Sessional: 30**

1. Introduction

Embedded systems overview, Design challenge, Processor Technology, IC Technology, Design Technology, Trade-offs.

2. Custom single-purpose processors: Hardware

Introduction, Combinational logic, Sequential logic, Custom single-purpose processor Design, RT-level custom single-purpose processor design, Optimizing custom single-purpose processors

3. General purpose processors : Software

Introduction, Basic Architecture, Operation, Programmer's view, Development environment, Application-Specific Instruction-set Processors, Selecting a Microprocessor.

4. Memory:

Introduction, Memory types, Memory Hierarchy and cache, Advanced Memory Interfacing : Communication Basics, Memory Access, I/O addressing, Interrupts, DMA, Arbitration, Multilevel Architecture, Protocols.

5. Microcontrollers:

Review 8051 Microcontroller Architecture & Programming.

Peripherals

Timers, Counters and Watchdog Timers, UART, Pulse width Modulators, LCD controllers Stepper Motor Controllers, Analog to Digital converters, Real-Time clocks.

An Exemplary Embedded Systems using Microcontrollers: Digital Camera

6. Introduction,

Specifications, Design.

7. State Machine and Concurrent process models: Introduction,

Models Vs. Languages, Text Vs. Graphics:

Textual Languages Vs. Graphical Languages, an Example, A Basic State Machine Model, FSM, FSM with Datapath Model: FSMD, Using State Machines, Concurrent Process Model, Communication among Processes.

**Text Books:**

1. Embedded System Design: A Unified Hardware/Software Introduction By Frank vahid / Tony Givargis John wiley& sons
2. The 8051 Microcontroller & Embedded Systems By Muhammad Ali Mazidi& Janice Gillispie Mazidi PHI

**References:**

1. Embedded Systems Architecture, Programming and Design By Raj Kamal TMH
2. Embedded Software Primer By Simon.
3. The 8051 Microcontroller: Architecture, Programming & Applications. By Kenneth J. Ayala Penram International. 2<sup>nd</sup> edn.

**Elective-IV**  
**DIGITAL IMAGE PROCESSING**

**Credits: 3**

**Subject Code: MTEI – 2.4**

**Exam Marks: 70**

**Semester-II**

**Sessional: 30**

1. Digital Image Fundamentals  
An image model – sampling & quantization – basic relation between pixels : imaging geometry.
2. Image Transforms  
Properties of 2-D fourier transforms, FFT algorithm and other separable image transforms, Walsh transforms, Hadamard, Cosine, Haar, Slant Transforms, RL Transforms and their properties.
3. Image Enhancement & Restoration  
Spatial domain methods, Frequency domain methods, Histogram Modification technique, Neighbourhood averaging, Median filtering, Low pass filtering, Averaging of Multiple Images, Image sharpening by differentiation, High pass Filtering, Degradation model for Continuous functions, Discrete Formulation, Diagonalization of Circulant and Block – Circulant Matrices, Effects of Diagonalization, Constrained and unconstrained Restorations Inverse filtering, Wiener Filter, Constrained least Square Restoration.
4. Image Encoding  
Objective an subjective Fidelity Criteria, the encoding process, the Mapping, the Quantizer and the Coder, Contour Encoding, Run length Encoding, Image Encoding relative to a Fidelity Criterion, Differential Pulse Code Modulation, Transform Encoding.
5. Image Compression  
Fundamentals, Image compression models, error free compression, lossy compression, image compression standards.
6. Image Segmentation  
The detection of Discontinuities, Point Line and Edge Detections, Gradient Operators, Combined Detection, Thresholding.
7. Image Representation  
Representation Schemes, Chain Codes, Polygon Approximation, Boundary Descriptors, Simple Descriptors, Shape Numbers, Fourier Descriptors.
8. Image Construction from Projections  
Radon Transforms, Convolution/filterback Projection.

**Textbooks**

1. Gonzalez RC & Woods RE, Digital Image Processing, Addison Wesley Publishing Company.
2. Jain AK, Fundamentals of Digital Image Processing, PHI
3. Rosefeld&Kak AC, Digital Picture Processing Academic Press Inc.



**AUDIT COURSE**

**Subject Code: MTEI – 2.5**

**Semester-II**

**Credits: 0**

**Exam Marks: 70**

**Sessional: 30**

## **INSTRUMENTATION LAB-II**

**Subject Code:** MTEI – 2.6  
**Semester-II**

**Credits:2**  
**Exam Marks:100**

## **MICROPROCESSOR LAB**

**Subject Code: MTEI – 2.7**

**Semester-II**

**Credits:2**

**Exam Marks:100**

**Elective-V**  
**LINEAR AND DIGITAL SYSTEMS DESIGN**

**Credits: 3**

**Subject Code: MTEI – 3.1**

**Exam Marks: 70**

**Semester-III**

**Sessional: 30**

Principles and applications of operational amplifiers as summers, Integrators controlled current voltage sources, Function generators, Logarithmic amplifiers, Anti logarithmic amplifiers, Instrumentation amplifiers, Sample and hold circuits, Comparators, Multivibrators, Window discriminators, Analog multipliers, Modulator circuits, Four quadrant multipliers, Squaring and square rooting, phase sensitive detector circuits, analog switches, multiplexers, Phase locked loops, broad band amplifiers, Precision rectifiers, IC voltage regulators, switched mode regulators and active filter circuits, 555, 566 \* 8038 ICs and their applications, Line drivers, Receivers for MODEMS, Isolation amplifiers.

**Text Books**

1. A.B. Grebene ‘Analog IC design’ – Van Nostrand
2. G.B. Clayton ‘Applications of Linear ICs’ – Max Millan (India)
3. Toetze & Schenk ‘Advanced electronic Circuits’ John Wiley

Review of combinational logic design : Logic design with MSI and LSI; Multiplexers and Demultiplexers; Arithmetic units; Carry look-ahead adders; Decimal and BCD adders / subtractors; Tabular design; Read only memory methods; programmable logic array methods. Analysis and synthesis of sequential circuits : Algorithmic state machine (ASM) methods; Map entered variable and synthesis of random logic. Fault detection and error correction in combinational and sequential circuits ; two level multi level multi level fault detection methods. Test generation; redundancy techniques. Introduction to computer aided design of digital circuits.

**Text Book**

1. S.C. Lee ‘Digital system design’ – Prentice Hall, May 2003 (Prescribed)

**References**

1. W.N. Carr ‘ MOS / LST Design and applications’ McGraw Hill
2. M.A. Bruer and A.D. Fridman ‘Diagnosis and reliable design of digital systems’ Computer science press, 1976.
3. ZVI Kohavi ‘Switching and Finite automata theory TMH 1976
4. Frederick J. Hill, Gerald R. Peterson ‘Computer Aided Logical Design with emphasis on VLSI – 1933 (John Wiley)

**Elective-V**  
**OPTICAL FIBERS AND APPLICATIONS**

**Credits: 3**

**Subject Code: MTEI – 3.1**

**Exam Marks: 70**

**Semester-III**

**Sessional: 30**

1. Optic Fiber Waveguides  
Step – Index Fiber, Graded – Index Fiber, Attenuation, Modes in Step-Index Fibers, Modes in Graded – Index Fibers, Pulse Distortion and Information Rate in Optic Fibers, Construction of Optic Fibers, Optic Fibers, Optic Fiber Cables,
2. Light Sources and Detectors  
Light-Emitting Diodes, Light-Emitting – Diodes Operating Characteristics, Laser Principles, Laser Diodes, Laser-Diode Operating Characteristics, Distributed – Feedback Laser Diode, Optical Amplifiers, Fiber Laser, Vertical-Cavity Surface-Emitting Laser Diodes  
Principles of Photodetection, Photomultiplier, Semiconductor Photodiode, PIN Photodiode, Avalanche Photodiode,
3. Couplers and Connectors  
Principles, Fiber end Preparation, Splices, Connectors, Source Coupling, Distribution Networks and Fiber Components, Distribution Networks, Directional Couplers, Star Couplers, Switches, Fiber Optical Isolator, Wavelength-Division Multiplexing, Fiber Bragg Gratings, Other Components : Attenuator, Circulator and Polarization Controller
4. Modulation, Noise and Detection  
Light-Emitting-Diode Modulation and Circuits, Laser-Diode Modulation and Circuits, Analog-Modulation Formats, Digital-Modulation Formats, Optic Heterodyne Receivers, Thermal and Shot Noise, Signal-to-Noise Ratio, Error Rates, Modal Noise, Amplifier Noise, Laser Noise, and Jitter, Additional Noise Contributors, receiver Circuit Design
5. System Design and Fiber Optical Applications  
Analog System Design, Digital System Design, Applications of Fiber Optics

**Text Book:**

1. Fiber Optic Communications, Joseph. C. Palais, Pearson Education, Asia, 2002

**References:**

1. Fiber Optic Systems, John Powers, Irwin Publications, 1997
2. Optical Fiber Communication, Howes M.J., Morgen, D.V John Wiely

## **Elective-V**

### **APPLICATION SPECIFIC INTEGRATED CIRCUITS (ASIC)**

**Credits: 3**

**Subject Code: MTEI – 3.1**

**Exam Marks: 70**

**Semester-III**

**Sessional: 30**

1. Introduction to ASICs – Types of ASICs, Design flow, Economics of ASICs, ASIC cell libraries, CMOS Logic, CMOS design rules, Logic cells, I/O cells, cell compilers.
2. ASIC Library Design – Transistors as resistors, Transistor parasitic capacitance, Logical effort, Cell design, Programmable ASICs, Programmable ASIC logic cells, Programmable ASIC I/O cells, Programmable ASIC interconnect, Programmable ASIC design software.
3. Low-level design entry, Schematic entry, low-level design languages, PLA tools, EDIF, An overview of VHDL and verilog, Logic synthesis, Simulation.
4. ASIC construction, Floor planning and placement.
5. CMOS System Core Studies  
Dynamic Warp Processors: Introduction, The problem, the algorithm, a functional overview, detailed functional specification, structural floor plan, physical design, fabrication, Hierarchical layout and design of single chip 32 bit CPU : Introduction, Design methodology, Technology updatability and layout verification.
6. Practical Realities and Ground Rules  
Further thoughts on floor plans/layout, floor plan layout of the four bit processors, input/output (I/O) pads, “Real estate”, further thoughts on system delays, ground rules for successful design, scaling of MOS circuits.

**Textbook:**

1. Application Specific Integrated Circuits by J.S. Smith, Addison Wesley, 1997.

**Reference Books:**

1. Basic VLSI Design : Systems and Circuits, Douglas A. Puckness & Kamran Eshraghian, Prentice Hall of India Private Ltd., New Delhi, 1989.
2. Principles of CMOS VLSI Design : A system perspective, N. Westle & K. Eshraghian, Addition – Wesley Pub. Co. 1985.
3. Introduction to VLSI System, C. Mead & L. Canway, Addison Wesley Pub Co. 1990.
4. The Design & Analysis of VLSI Circuits, L.A. Glassey & D.W. Dobbephil, Addison Wesley Pub Co. 1985.
5. Introduction to NMOS & VLSI System Design, A. Mukharjee, Prentice Hall, 1986.
6. VLSI Design Techniques for analog and digital circuits, R.L. Geiger, P.E. Allen & N.R. Streder, McGraw Hill Int. 1990.
7. Digital Integrated Circuits, A Design Perspective, Jan A. Rabey, Prentice Hall of India Pvt. Ltd., 1997.
8. Application specific integrated circuits, J.S. Smith, Addison Wesley, 1997.

## Core Elective: VLSI

**Credits: 3**

**Subject Code: MTEI – 3.2**

**Exam Marks: 70**

**Semester-III**

**Sessional: 30**

1. Digital Systems and VLSI : why design integrated circuits? – integrated circuits manufacturing – CMOS Technology – Integrated Circuit Design Techniques.
2. Transistors and Layout : Introduction – Fabrication processes – transistors – wires and vias – design rules – layout design and tools.
3. Logic Gates : Introduction – combinational logic functions – static complementary gates – switch logic – low power gates – delay through resistive interconnect-delay through inductive interconnect.
4. Combinational Logic Networks : Introduction – standard cell – based layout – simulation – combinational network delay – logic and interconnect design – power optimization – switch logic networks – combinational logic testing.
5. Sequential Machines : Introduction – latches and flip-flops – sequential systems and clocking disciplines – sequential system design – power optimization – design validation – sequential testing.
6. Subsystem Design : Introduction – subsystem design principles – combinational shifters – address – high density memory – field – programmable gate arrays – programmable logic arrays – floorplanning methods – off-chip connections.

### **Text books:**

1. Modern VLSI Design, System – on – Chip by Wayne Wolf, Pearson Education, 3<sup>rd</sup> Edition.

### **References:**

1. Introduction to VLSI Systems by C. Mead and L. Conway, Addison Wesley, 1980.
2. Introduction to VLSI Design by Eugene D. Fabrecius, McGraw Hill, 1990.
3. Basic VLSI Design by D.A. Pucknell& K. Eshragian, PHI, 3<sup>rd</sup> Edition.

